



## EMC BASICS Part 1

Now... this is what will happen to your designs in real life!

Strictly Private and Confidential

Agenda



- Technical terminology
- EMC Definitions
- Source, Path, Victim examples
- How is EMC made?
- What Is The Vehicle EMC Environment?

#### ESD

- Material and Components Behavior
- Shielding and grounding concepts

#### Test setups

EMC Simulations



## What Is EMC?



#### ■EMC → Electromagnetic compatibility

■Definition: an electronic device's ability to coexist with other devices without causing functional disturbances & not be disturbed by other devices

#### ■EMI → Electromagnetic interference

■Definition: Disruption of an electronic device's function(s) due to conducted or radiated electromagnetic energy into the device

#### ■RFI → Radio frequency interference

Definition: See EMI definition

#### ■ESD → Electrostatic discharge

Definition: Sudden transfer of charge from one object to another

#### **EMC Fundamentals**



#### What makes an EMC problem?

#### What is EMC:

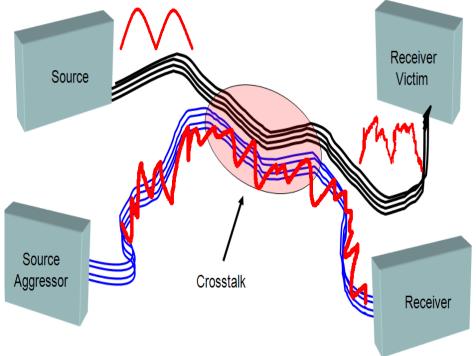
Ever wonder why you are requested to switch off your electronics devices when you are taking a flight? This is one of the typical case of EMC of which your electronics devices may interfere with the electronics system in the aircraft.

#### EMC problem basics:

-Source of EM energy (Emitter; Source) -Transfer of EM energy (Coupling path) -Reception of EM energy (Receiver; Victim)

#### Coupling mechanisms:

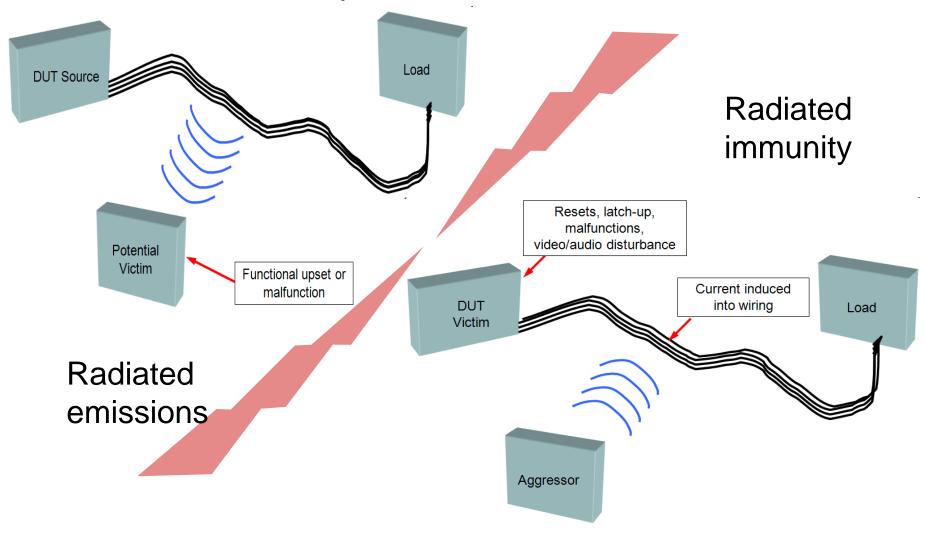
-Conducted coupling -Capacitive coupling -Inductive coupling -Radiated coupling



#### **EMC** Fundamentals

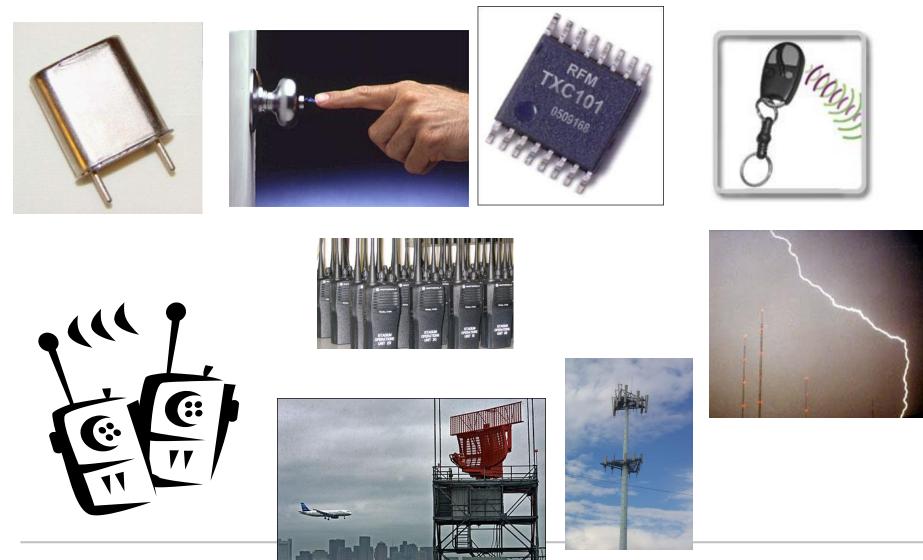


#### What makes an EMC problem?



#### Examples of Sources





#### Sources

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#### Electric and magnetic field Sources:

**Electric fields** are created by differences in voltage: the higher the voltage, the stronger will be the resultant field. **Magnetic fields** are created when electric current flows: the greater the current, the stronger the magnetic field.

#### **Electric fields**

Electric fields arise from voltage (dv/dt) Their strength is measured in Volts per meter (V/m)

An electric field can be present even when a device is switched off.

Field strength decreases with distance from the source.

#### **Magnetic fields**

Magnetic fields arise from current (di/dt). Their strength is measured in amperes per meter (A/m).

Magnetic fields exist as soon as a device is switched on and current flows.

Field strength decreases with distance from the source.

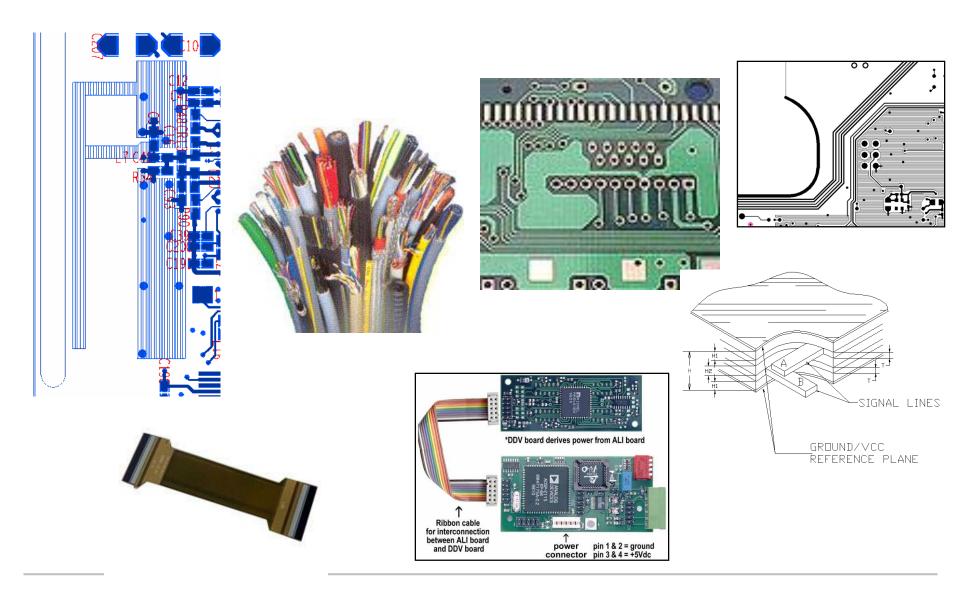




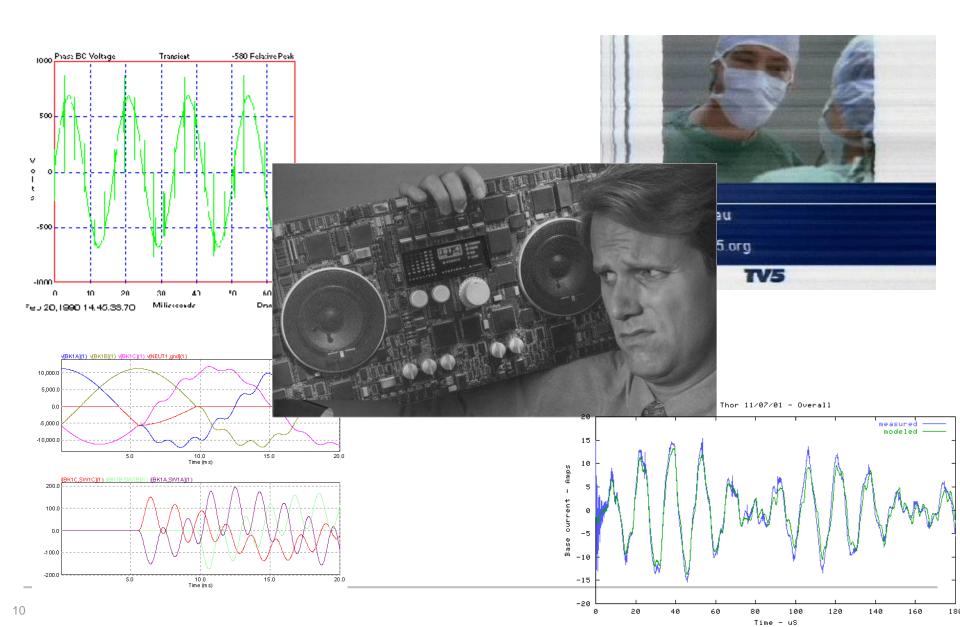


#### **Examples of Paths**











#### How is EMC made?

#### The coupling path is frequency dependent

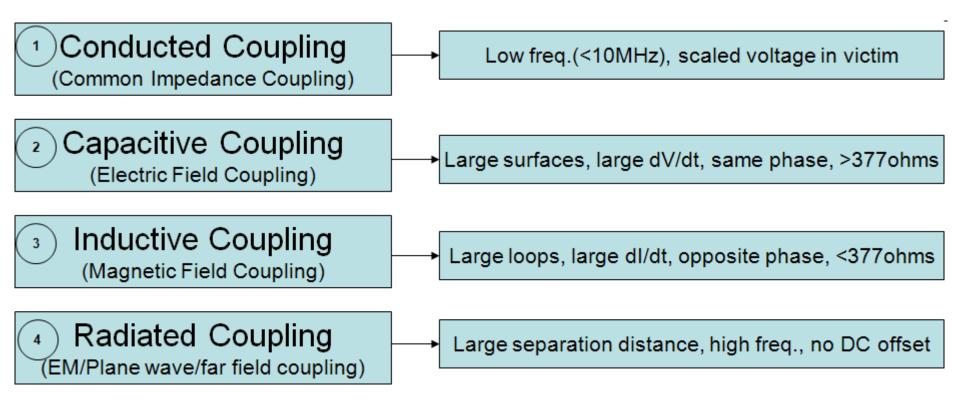
- High frequencies are typically radiated
- Low frequencies are typically conducted
- The boundary is typically about 30 MHz

#### There are 5 aspects to understanding EMC

- Frequency Where in the spectrum is the problem observed?
- Amplitude How strong is the energy source?
- Time Is it continuous or not?
- Impedance What is the Z of the source and receiver?
- Dimensions -What are the physical dimensions of the device which will allow emissions?
- (RF currents will leave through openings which are fractions of a wavelength!)



#### **Coupling Clues**

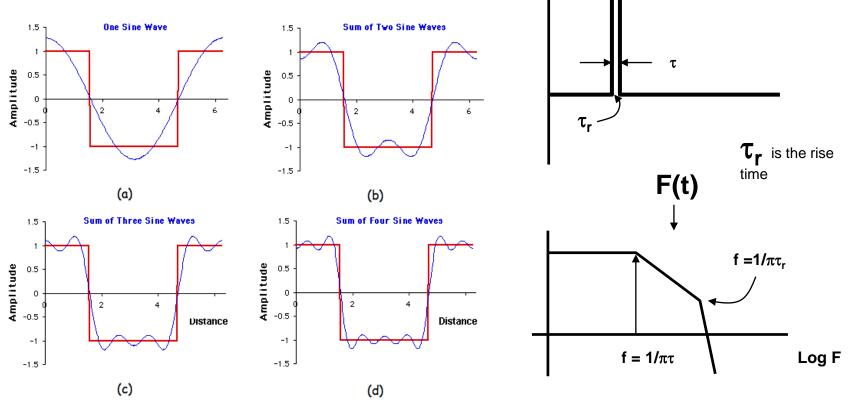


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#### Think Harmonics!

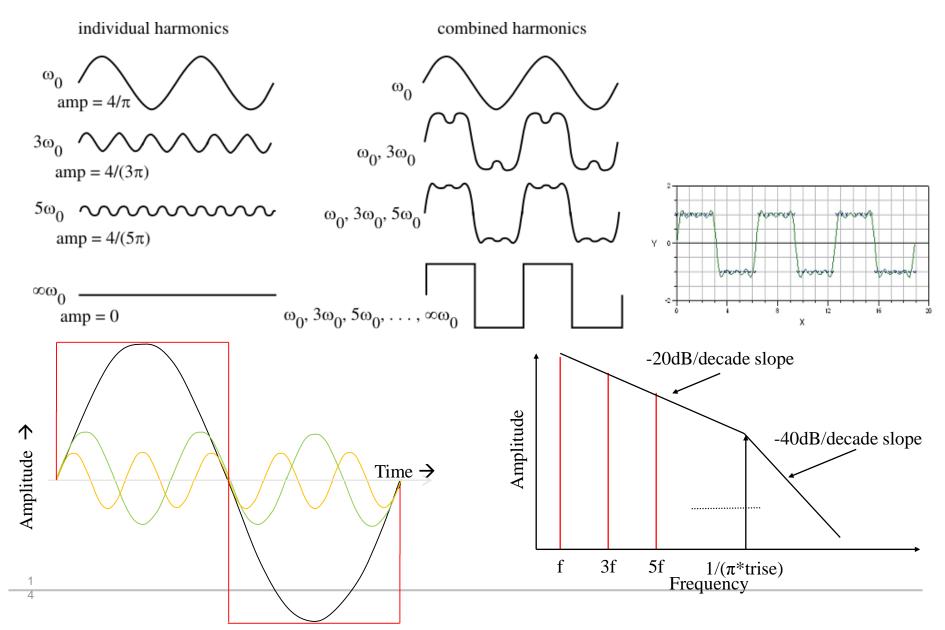
#### Fourier transformation:

Signal rise and fall times are one of the main reason for EMI. The smaller these times are – the more issues we should expect.



#### **Clock Waveform Analysis**





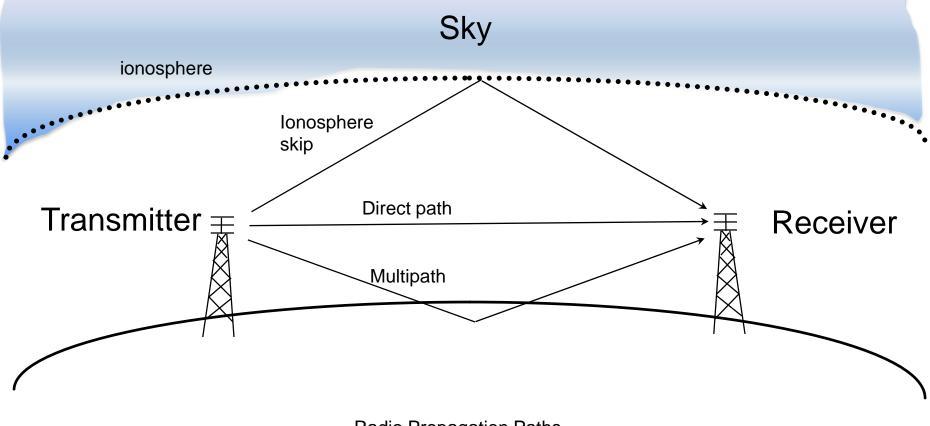


# What is the vehicle EMC environment?

# Why do we have EMC normative?



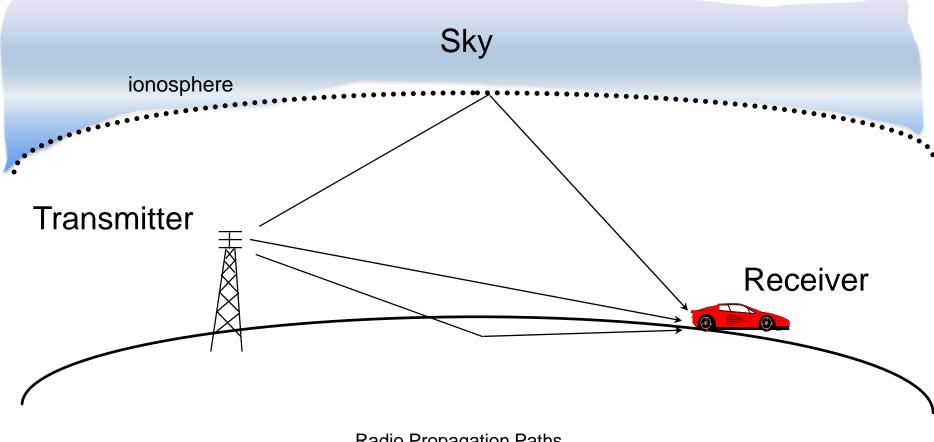
#### Land Based Communications



**Radio Propagation Paths** 



#### Vehicle Exposure



**Radio Propagation Paths** 



#### Intentional RF Transmitters













#### **Un-Intentional Transmitters Found In Vehicle Environment**









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#### Vehicle RF Receivers



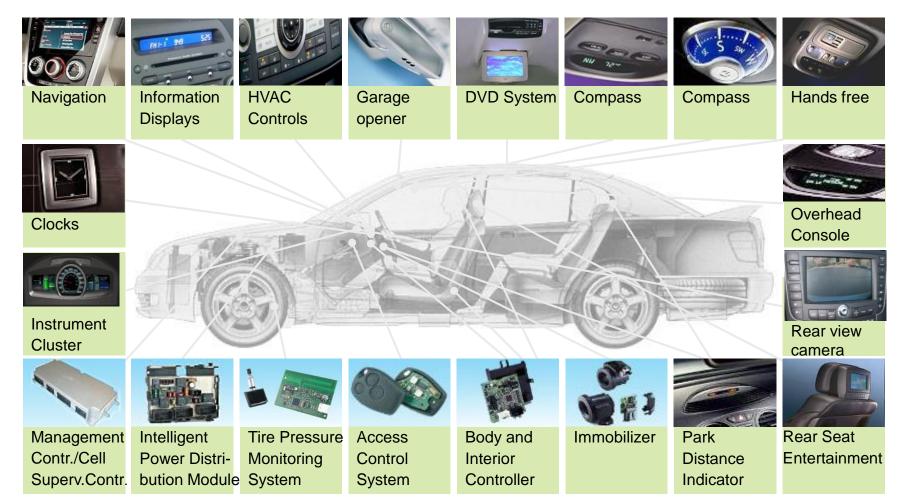








#### **On-board Electronics**





# EMC COMPLIANCE Global View

#### **EMC** Compliance

#### Overview



23

<u>Visteon</u> Product Development

Component Level Tests



<u>OEM</u> Integration

Perform onboard receiver and full-vehicle validation



#### <u>Regulatory</u> (Government)

Obtain necessary certification prior to mass production





Industry Canada



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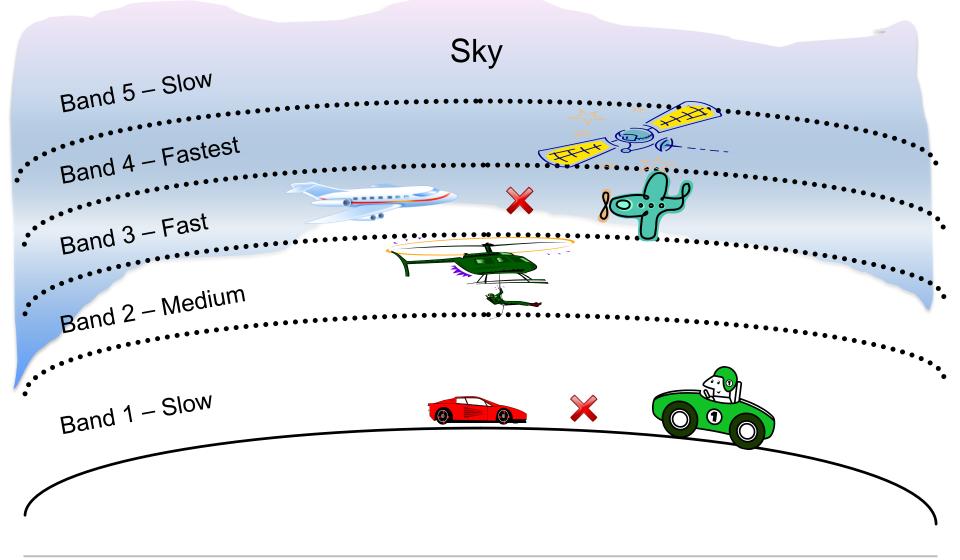




#### **EMC** Compliance

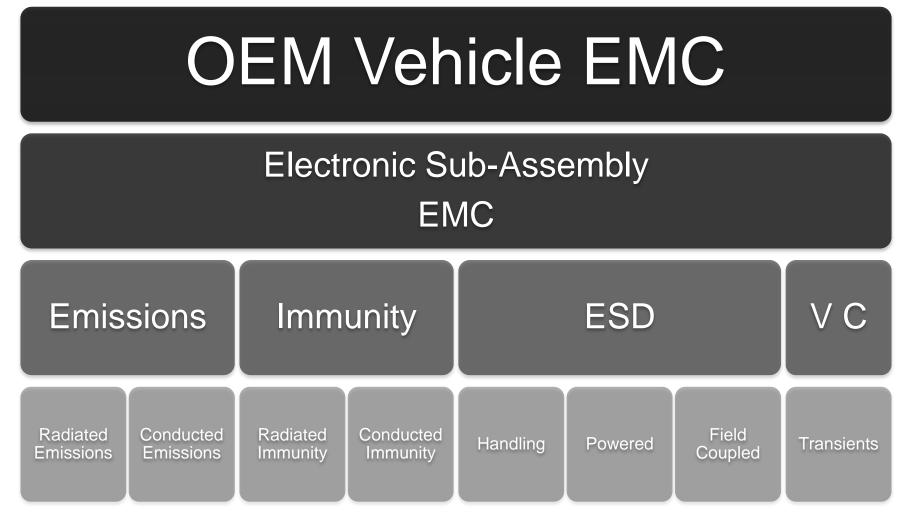


#### **Frequency Bands**





#### Hierarchy



#### **EMC** Compliance

#### Vehicle EMC Validation



### Vehicle level EMC testing by OEM

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Radiated emissions Radiated immunity On-board transmitters Off-board transmitters ESD testing

Vehicle in drive mode

Rotate vehicle to test all sides



#### EMC Fundamentals

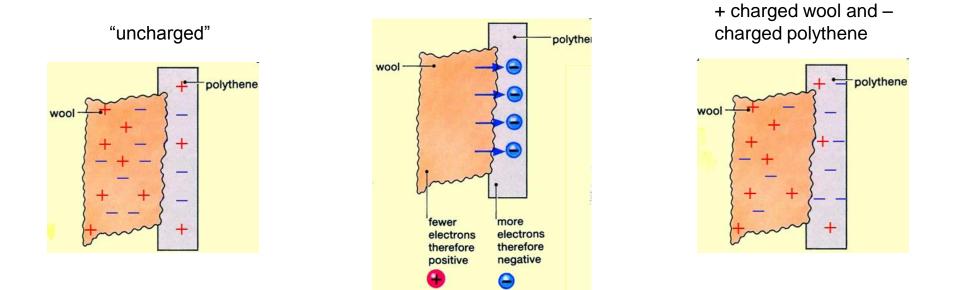
#### Electrostatic Discharge

Means of static generation	<u>RH 10-20%</u>	<u>RH 65-90%</u>
Walking across a carpet	35,000 V	1,500 V
Walking on a vinyl tile floor	12,000 V	250 V
Vinyl envelopes for work instructions	7,000 V	600 V
Worker at bench	6,000 V	100 V



#### How is ESD made?

Electrostatic Charging: The process of gaining electrons (negative charging) or losing electrons (positive charging). It is most important to know that it is only the negative electrons which can move. Positive charges (protons) cannot move.



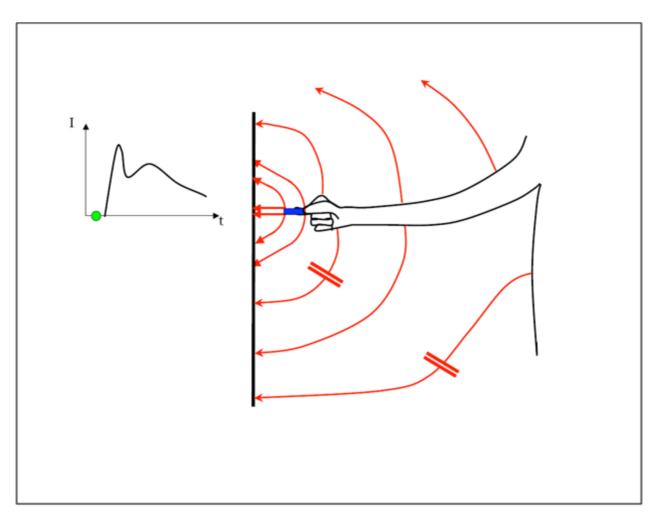
Both objects are now equally charged, with opposite charges because only electrons have moved!



#### **ESD** Fundamentals



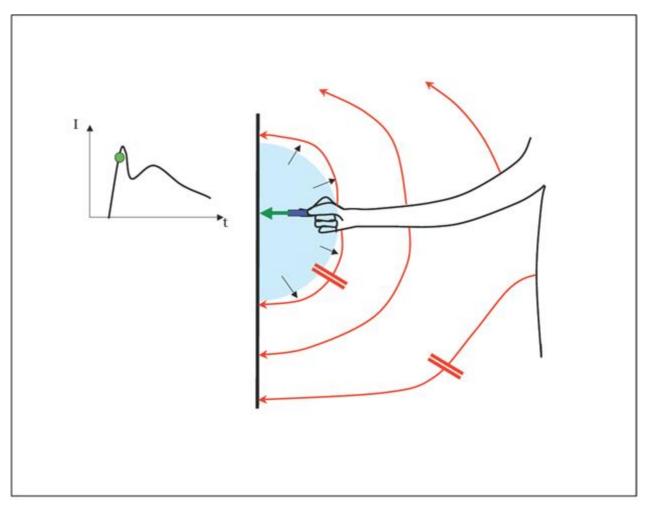
#### Human Body Model – I



#### **ESD** Fundamentals



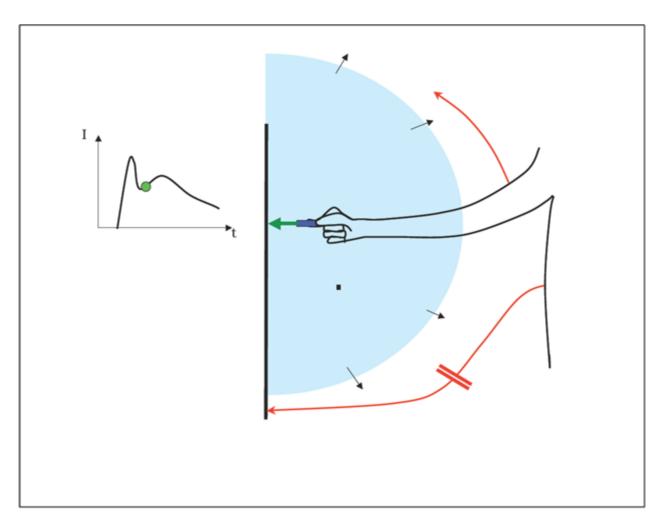
#### Human Body Model – II



#### **ESD** Fundamentals



#### Human Body Model – III

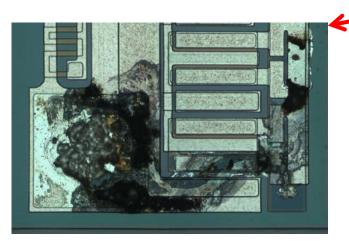




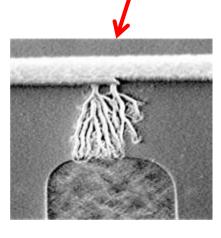
#### Effects of ESD on Electronic Equipment

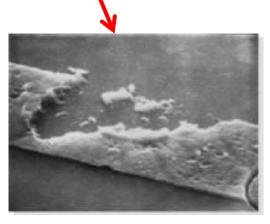
■All ESD damage is not created equal. In fact, there are three different types of ESD damage:

- A catastrophic failure
- Latent defect
- An upset failure



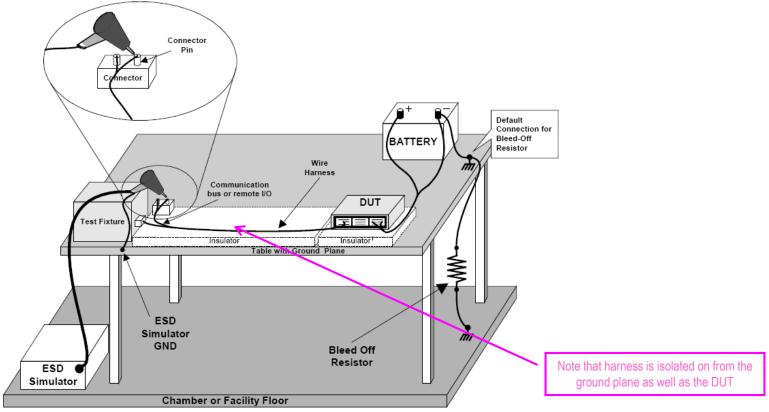
If the electrostatic discharge possesses sufficient energy, damage could occur in the device due to localized overheating.







Electro Static Discharge (ESD) – Test Setup For Powered Case

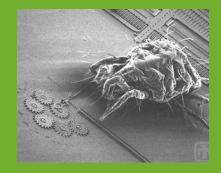


Bleed Off resistor used to bleed charge from discharge points



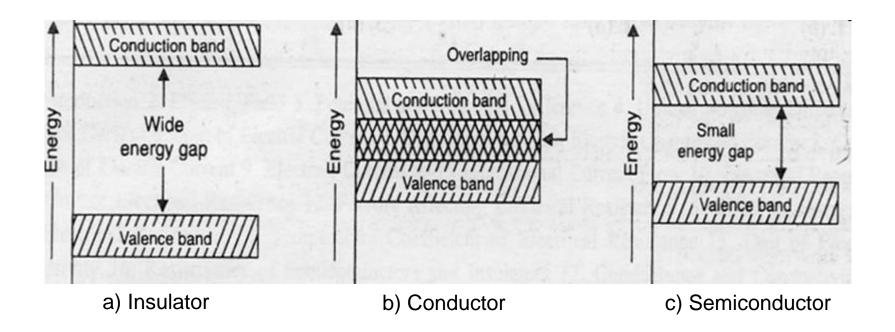
#### EMC Fundamentals

#### Material Behavior





In a conductor, electric current can flow freely, in an insulator it cannot. But why?







Metal = Conductor



By definition all metals are conductors of electricity. Some conduct better than others.

Material	Relative Conductivity
Silver	106
Copper	100
Gold	65
Aluminum	59
Lead	7

"Conductor" implies that the outer electrons of the atoms are loosely bound and free to move through the material. Metals are also generally good heat conductors.





Dielectric **≠** Insulator



Dielectrics are used to store the electric charges, while insulators are used to block the flow of electric charges (they more or less act like a wall).

While all dielectrics are insulators (they don't allow the flow of electric charges through them), all insulators aren't dielectric because they can't store charges unlike dielectrics.

Material	Volume resistivity ( $\Omega/m$ )
P.E.T. (Polyethylene)	10 <sup>15</sup> –10 <sup>17</sup>
Polythene (high density)	10 <sup>14</sup> –10 <sup>15</sup>
P.V.C.	5 × 10 <sup>12</sup> –10 <sup>13</sup>
Diamond	10 <sup>10</sup> –10 <sup>11</sup>
Rubber	10 <sup>9</sup>

#### EMC Fundamentals

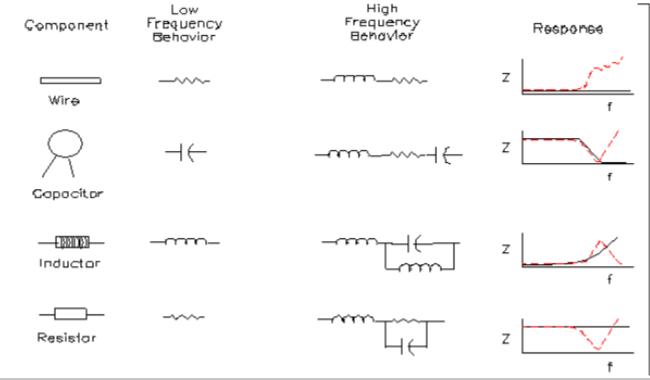


#### **Component Behavior**

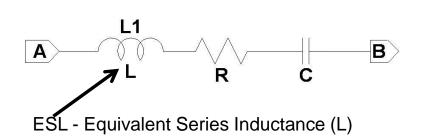


All passive components have resistance, capacitance and inductance

Component behavior is different at low and high frequencies





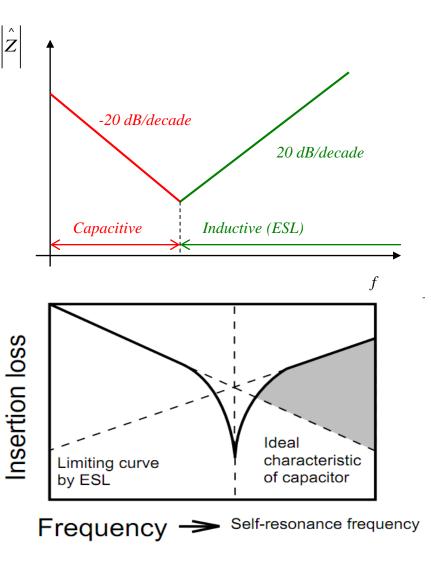


#### Self-resonance frequency:

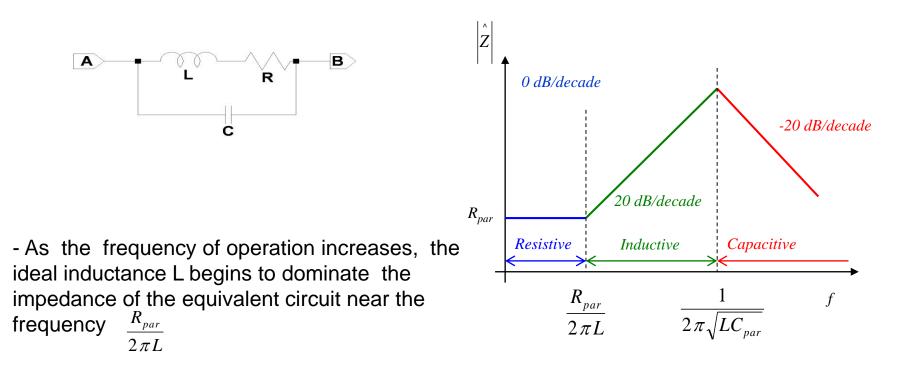
The frequency at which resonance occur due to the capacitor's own capacitance and ESL. It is the frequency at which the impedance of the capacitor becomes zero.

$$j2\pi fL + 1/j2\pi fC = 0$$

$$rac{1}{2\,\pi\sqrt{L_{lead}C}}$$



#### **Component Behavior (Inductors)**



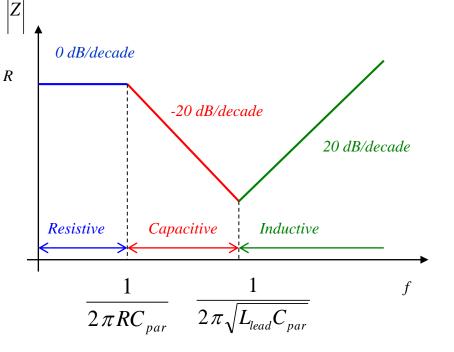
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- As the frequency increases further, the impedance of the parasitic capacitance decreases until it's magnitude is equal to that of the ideal inductance. This occurs at the self resonant frequency  $\frac{1}{2\pi\sqrt{LC_{par}}}$ 

#### **Component Behavior (Resistor)**



- As the frequency increases beyond this point, more current begins to flow through the conducting path provided by the parasitic capacitance than flows through the bulk resistance. In this regime, the lead inductance remains small (i.e., nearly a short circuit).

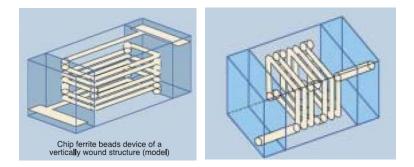


- As the frequency increases further, the impedance of the equivalent circuit decreases until the lead inductance and the parasitic capacitance cause the resistor to resonate. The equivalent circuit impedance is a minimum at the self resonant frequency of the resistor  $\frac{1}{2\pi\sqrt{L_{lead}C_{par}}}$ 

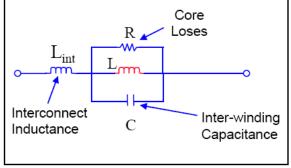
Note: Not as significant change vs frequency as compared to a capacitor impedance curve



Ferrites are composed of a metallic conductor which is wound horizontally or vertically inside a ferrite material core and connected to SMD pads



Equivalent circuit for a ferrite bead uses resistance to model the losses in the core material
Core



#### **EMC** Fundamentals

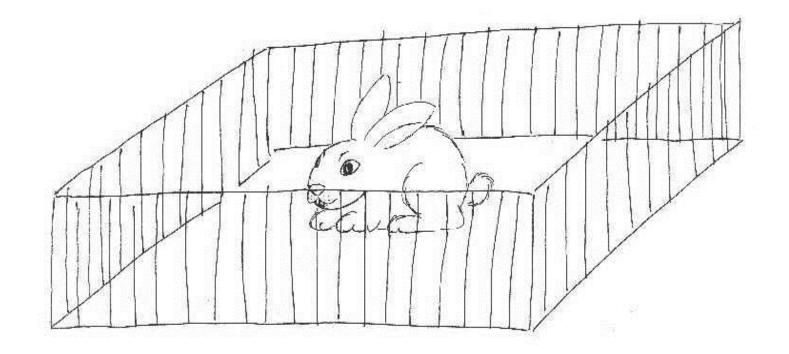
#### Shielding Concepts





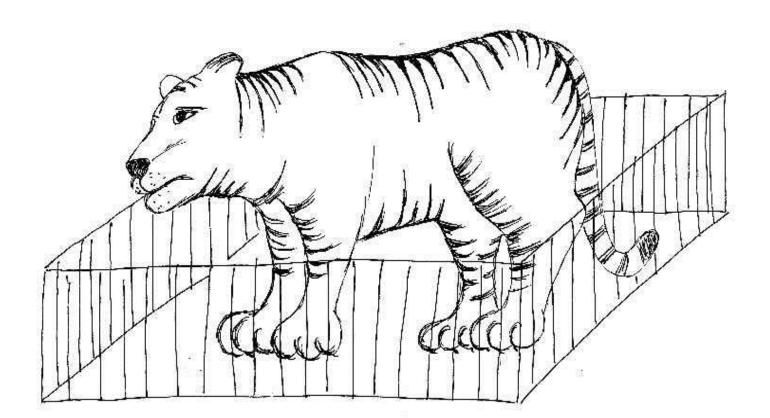




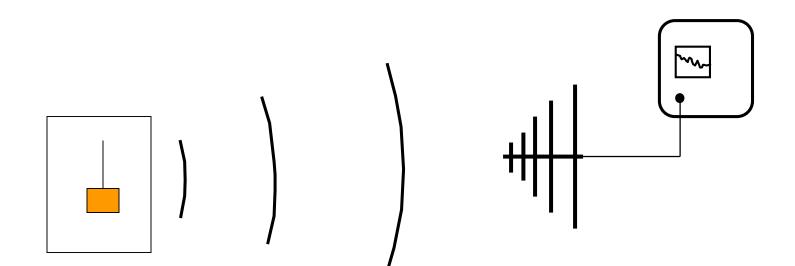


#### Shielding (What is a shield?)

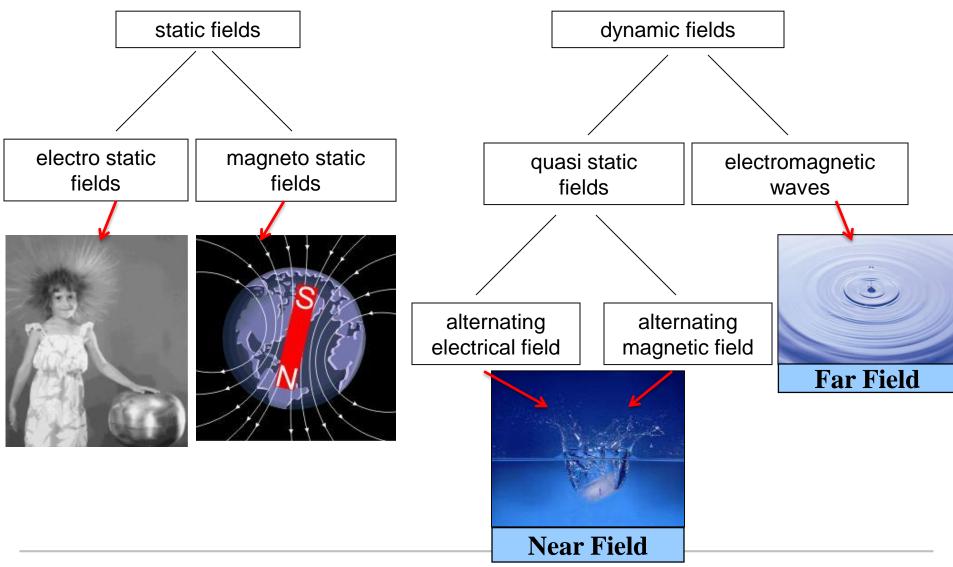








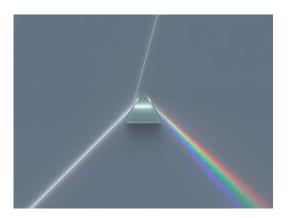


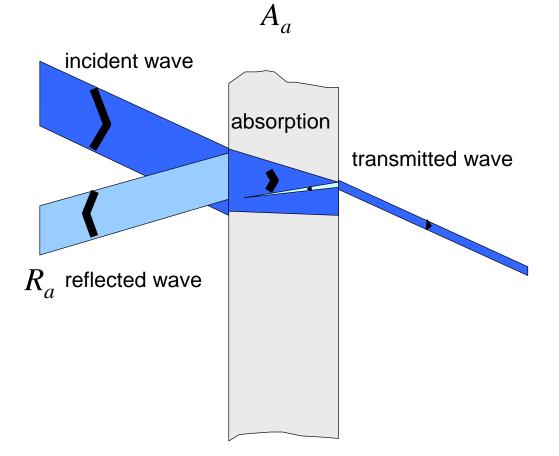




#### **Shielding Effectiveness:**

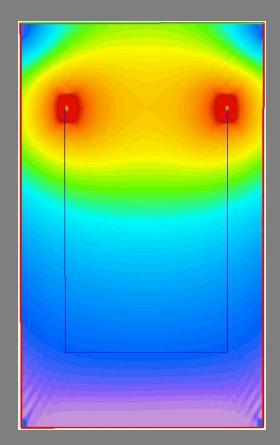
$$SE\big|_{dB} = R\big|_{dB} + A\big|_{dB}$$





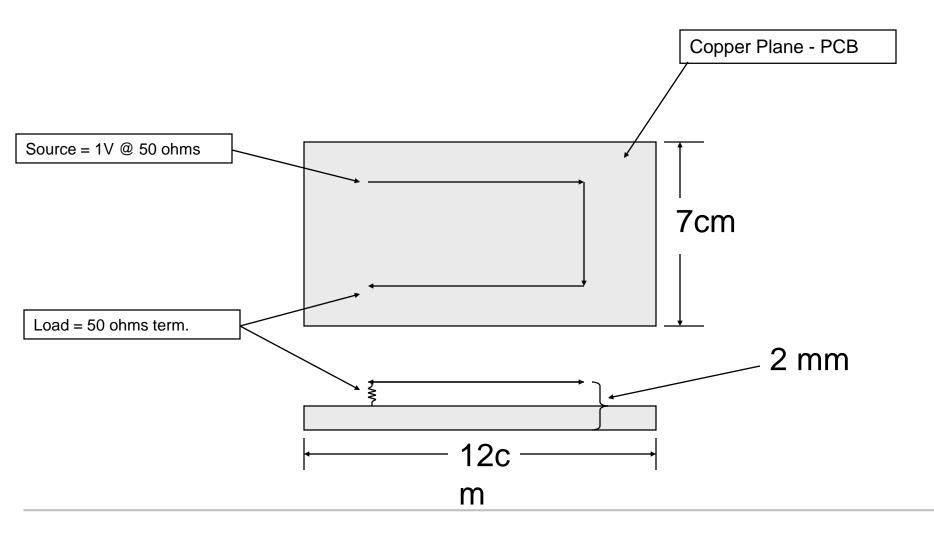
#### **EMC** Fundamentals

### RF Current Return Path Concepts





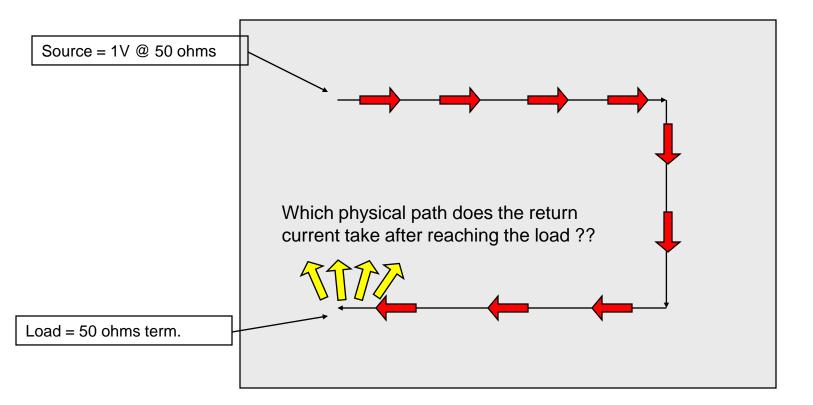
• 3" x 5" coupon board





= Source current to 50 ohm load termination

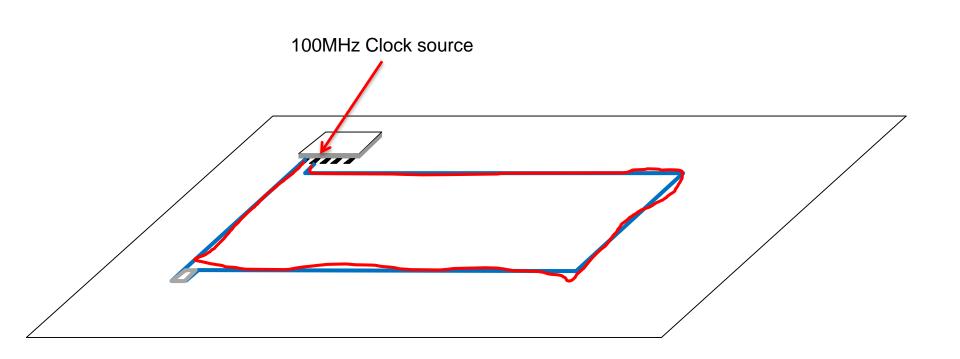
= Return current from 50 ohm load back to 50 ohm source





# 1kHz 100kHz 500kHz 1MHz 10MHz 100MHz 1kHz 100kHz 500kHz 1MHz 100Hz 100Hz 100Hz 100Hz

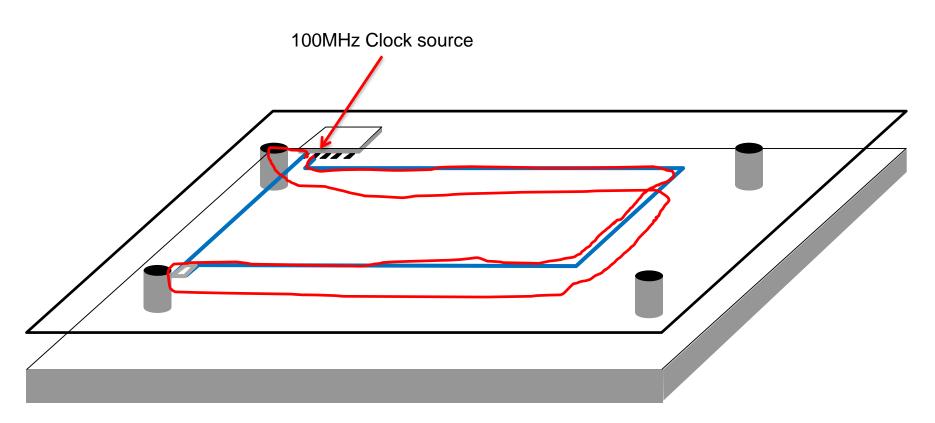
#### Grounding (Current Returns In The GND Plane)



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Big loop  $\rightarrow$  good magnetic source  $\rightarrow$  increased EM emissions

Grounding (Current Returns In The Metal Sheet)



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Longer path for current, but reduced loop area  $\rightarrow$  decreased emissions

#### Grounding (Conclusion)



- Lots of very low inductive connections
- No isolating coating on the metal
- Avoid corrosion and oxidation  $\rightarrow$  Good EMC performance over the whole lifetime
- Mechanical tolerances can lead to isolated connections
- Control of the return current paths







# EMC COMPONENT LEVEL TESTS





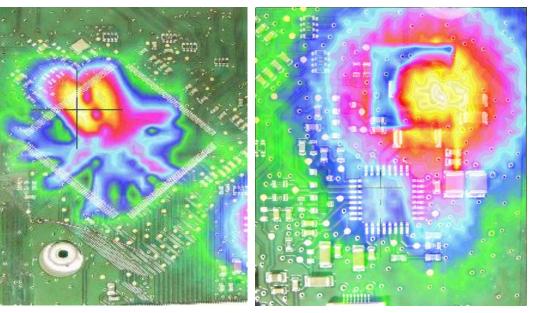
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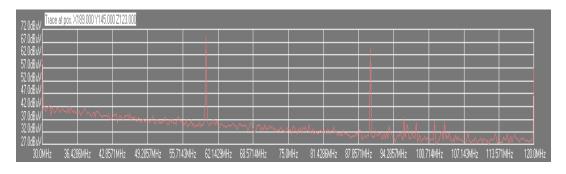


#### Pre-compliance tests are made during prototype development.

• Near Filed Scan – EMC Scanner

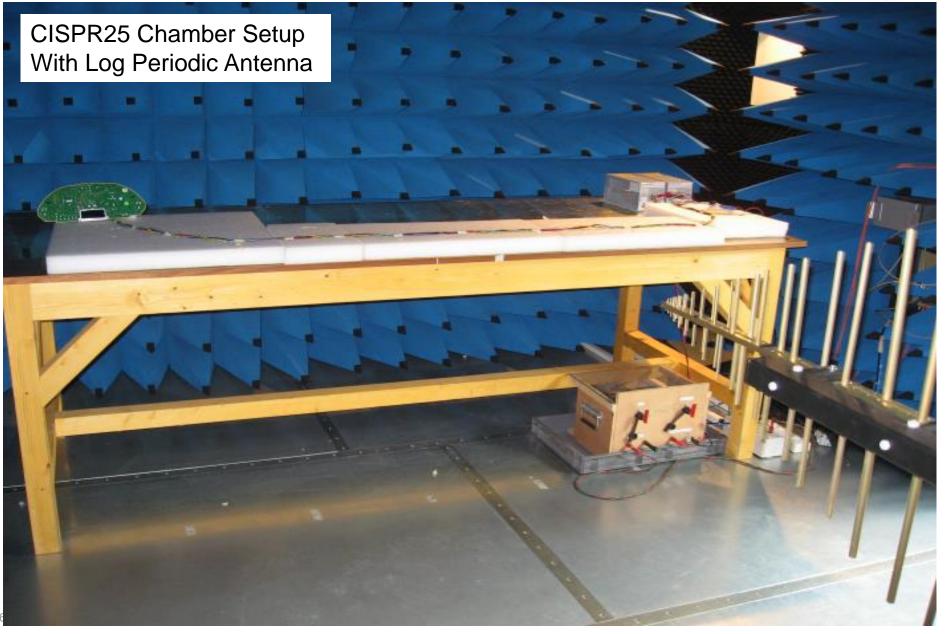






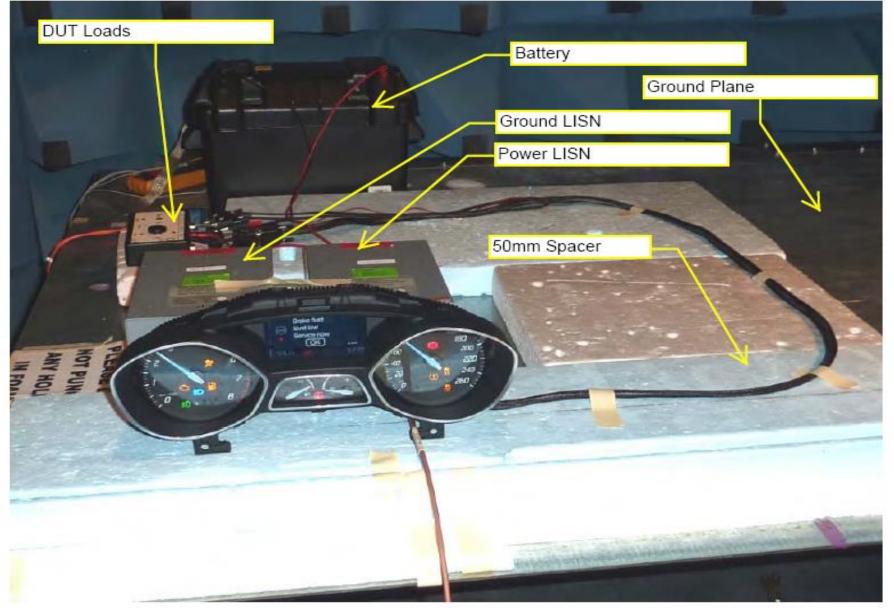
#### **Radiated Emissions Measurements**





#### **Conducted Emissions Measurements**





#### **Basics of Immunity Problem**

**Bulk Current Injection Measurement** 

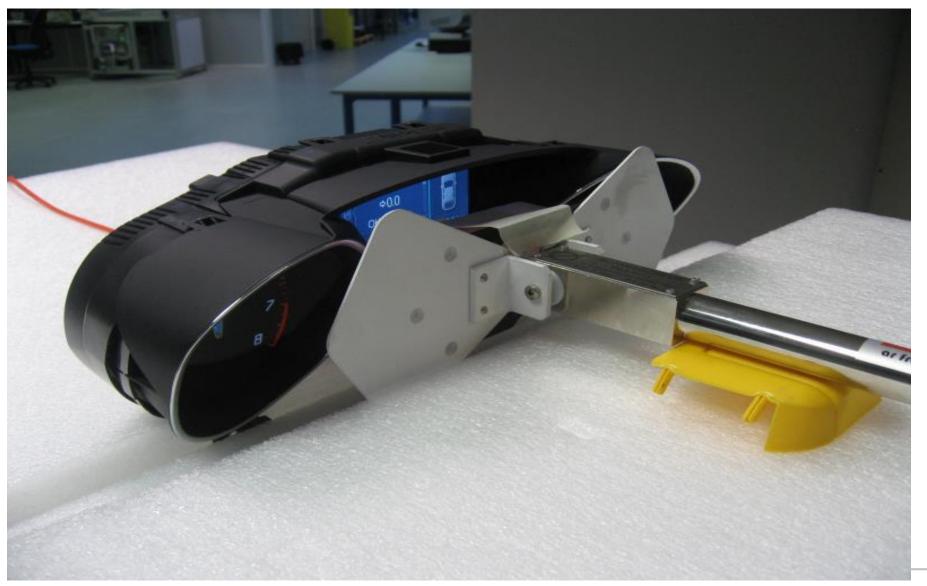




#### **Basics of Immunity Problem**

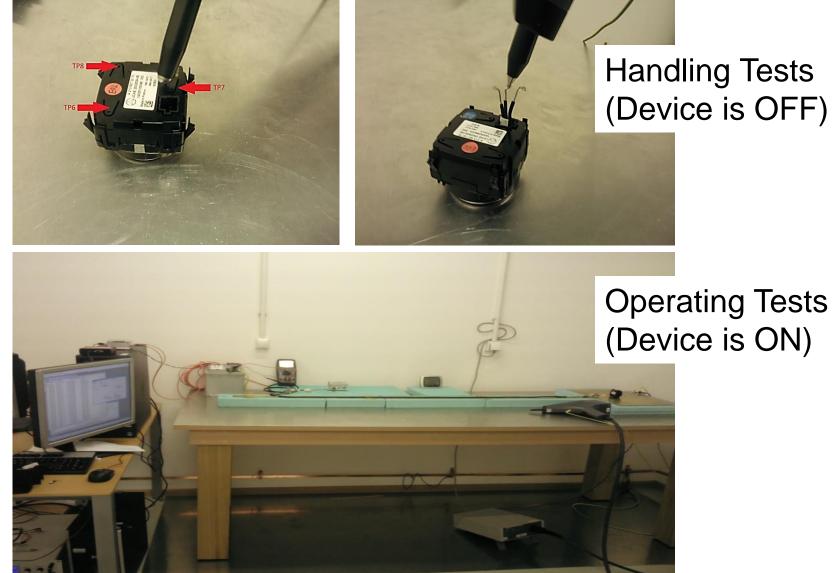
Near-Field Immunity Measurement – Cell Phone Simulator

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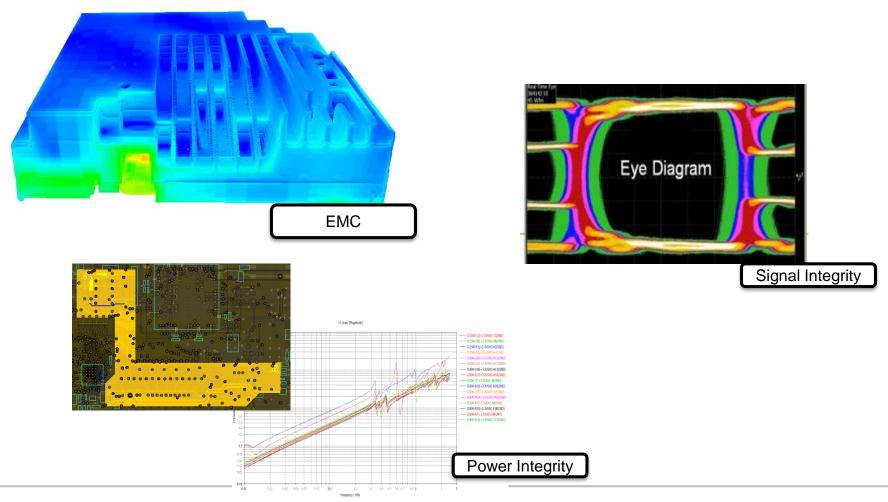
#### **ESD** Tests



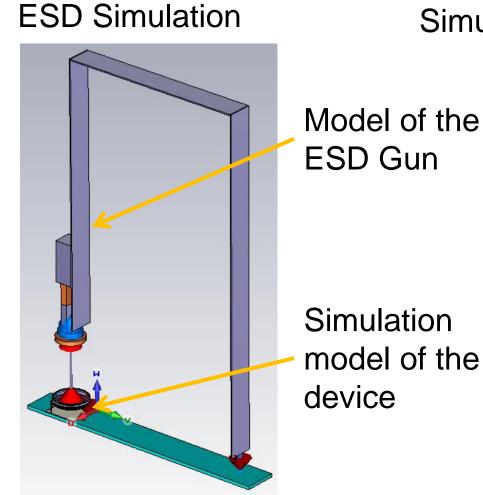




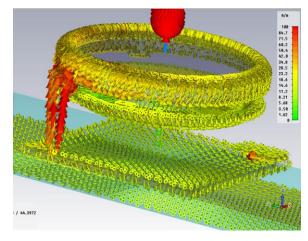
## **EMC SIMULATIONS**



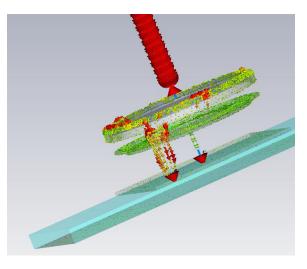




#### Simulation Results: Surface current



Simulation model of the

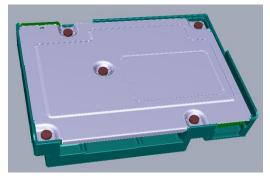


#### **EMC** simulations

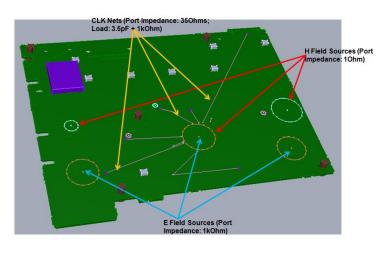


#### **Radiated Emissions Simulation**

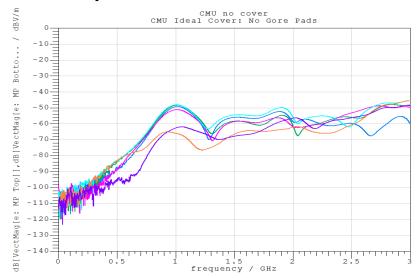
#### Model

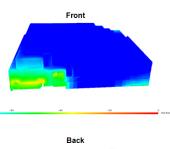


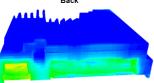
#### Model with defined sources



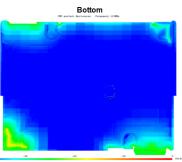
# Results at monitoring points at 1m







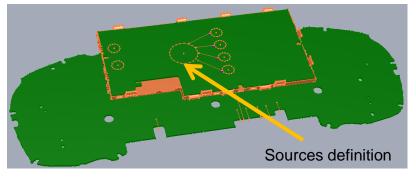




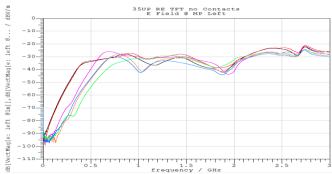
#### **EMC** simulations



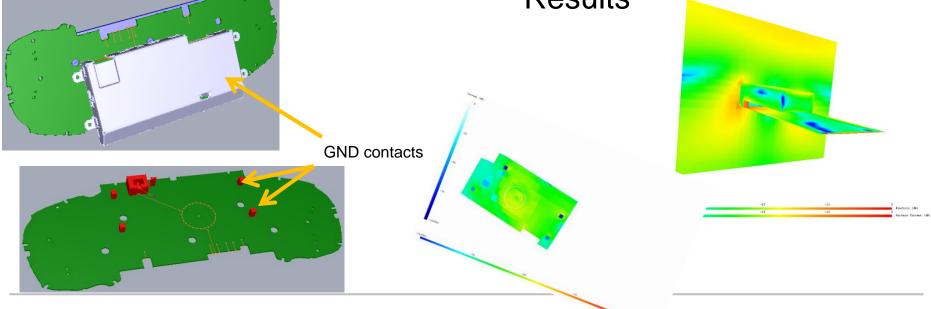
#### Shielding Effectiveness Simulation Model



#### **E-field Results**

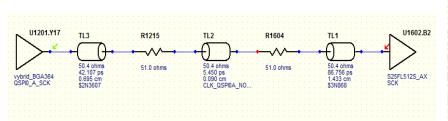


#### E-field and Surface Current Results



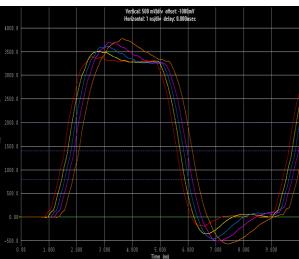
#### Signal Integrity Simulation

#### Example for selecting termination values Trace built in Simulation tool Trace from

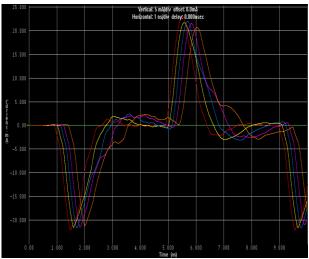


#### O-scope results

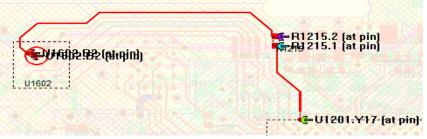
#### Voltage waveform



#### Current waveform

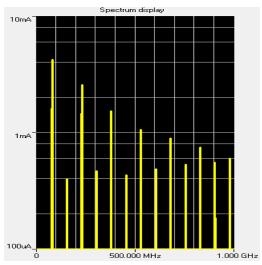


#### Trace from layout



#### Freq. domain current

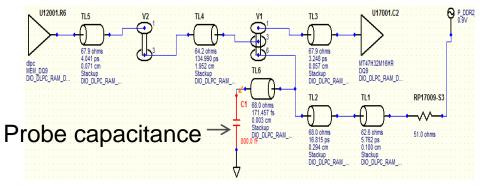
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#### Signal Integrity Simulation

#### Correlation of the models is very important

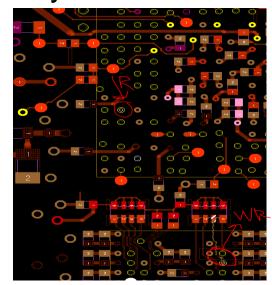
#### Build model of the transmission line



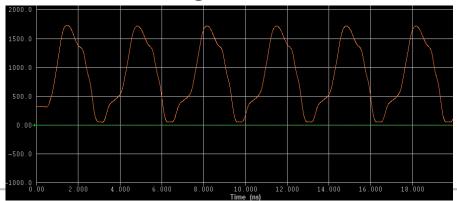
#### Measured signal



#### Layout view



#### Simulated signal

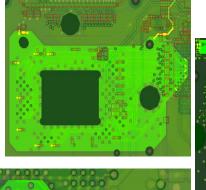


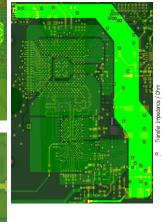


#### **Power Integrity Simulation**

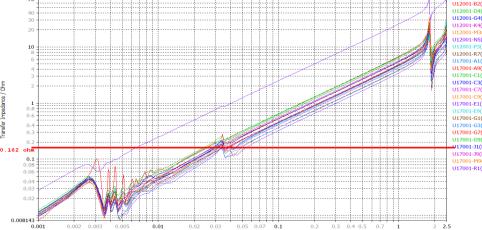


#### Different type of power analyzes





#### Impedance over Frequency graph



Frequency / GHz

1782.4

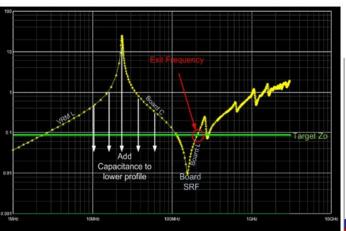
1782.11

1782.04

1781.97

1781.9

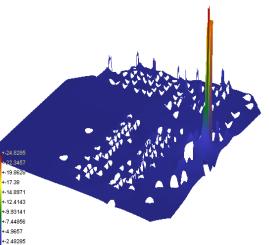
112001-D4(P\_1V8\_DLPC\_DDR)-C6(GND) U12001-G4(P\_1V8\_DLPC\_DDR)-G7(GND 112001-K4(P 1V8 DLPC DDR)-K 2001-N5(P\_1V8\_DLPC\_DDR)-L4(GND 112001-R7(P 1V8 DLPC DDR)-T 117001-49/P 1V8 DLPC DDR)-B8(GND 1117001-C1(P\_1V8\_DLPC\_DDR)-B2(GND) U17001-C3(P 1V8 DLPC DDR)-E3(GND) U17001-G1(P\_1V8\_DLPC\_DDR)-F2(GND) U17001-G9(P\_1V8\_DLPC\_DDR)-17001-11(P\_1V8\_DLPC\_DDR)-13(GND) J17001-J9(P\_1V8\_DLPC\_DDR)-H8(GND) U17001-R1(P\_1V8\_DLPC\_DDR)-N1(GND)



Graph legend

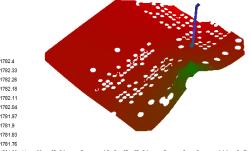
- Target Z0 = target impedance of power distribution network (PDN) VRM L = voltage regulator models inductive part
- Board C = PCB or plane capacitance
- Board SRF = first self resonant frequency of PCB or plane
- Exit frequency = frequency up to which PDN is capable to deliver energy below Z0

#### **DC Current Density**



#### 0 A/mm² Max Current Density: 24.8 A/mm<sup>2</sup>, Current Density for layer "1": 24.8 A/mm

#### **DC Drop Voltage**



1781.68 m<sup>\</sup> Max Voltage Drop: 18.3 mV, Voltage Drop for layer "1": 0.7 mV

#### End of Part 1



